

| No. | NAME          | I/O | Description  | P-OFF   | P-SAVE | P-FAIL | Reset |
|-----|---------------|-----|--|---------|--------|--------|-------|
| 92  | S TAB_ON(L)   | I   | Input terminal of condition of the erasure prevention tab on the cassette tape.<br>*With tab : "Low" / Without tab : "High". | In      | In     | In     | In    |
| 93  | T.REEL.PULSE  | I   | Input terminal for the pulse from Take-up Reel Sensor.   | In      | In     | In     | In    |
| 94  | S.REEL.PULSE  | I   | Input terminal for the pulse from Supply Reel Sensor.  | In      | In     | In     | In    |
| 95  | DAVN          | I   | DAVN signal from Slicer  | In      | In     | In     | In    |
| 96  | EEP_WR        | O   | Write enable for EEPROM<br>H:READ only L:Write   | High    | High   | Low    | High  |
| 97  | MAIN IIC CLK  | O   | IIC clock for AV1CHIP,FM audio,Tuner,Decoder and RFC   | Not fix | Low    | Low    | Low   |
| 98  | MAIN IIC DATA | I/O | IIC data for AV1CHIP,FM audio,Tuner,Decoder and RFC  | Not fix | Low    | Low    | Low   |
| 99  | OSD IIC CLK   | O   | IIC clock for EEPROM,I/O and slicer  | Not fix | Low    | Low    | Low   |
| 100 | OSD IIC DATA  | O   | IIC data for EEPROM,I/O and slicer   | Not fix | Low    | Low    | Low   |
| 101 | CNR_OFF(H)    | O   | Control terminal for the Chroma Noise Reduction.<br>*CNR ON : "High" / CNR OFF : "Low".                                      | Low     | Low    | Low    | Low   |
| 102 | EX.FF/REW(L)  | O   | Control terminal for the filter of the PB-CTL signal during FF/REW.  | Low     | Low    | Low    | Low   |
| 103 | FG.AMP.OUT    | O   | Output from internal FG Amplifier  | ---     | ---    | ---    | ---   |
| 104 | FG.AMP.IN     | I   | Input for internal FG Amplifier  | ---     | ---    | ---    | ---   |
| 105 | GND(A)        | I   | GND.   | ---     | ---    | ---    | ---   |
| 106 | to GND        | I   | GND.   | ---     | ---    | ---    | ---   |
| 107 | PFG           | I   | FPG input terminal.  | ---     | ---    | ---    | ---   |
| 108 | OREF          | O   | Output from internal reference voltage (2.5V)  | ---     | ---    | ---    | ---   |
| 109 | IREF          | I   | Input for internal reference voltage   | ---     | ---    | ---    | ---   |
| 110 | to GND        | I   | GND  | ---     | ---    | ---    | ---   |
| 111 | CTL.HEAD(-)   | I   | Input signal from CTL HEAD(+)  | ---     | ---    | ---    | ---   |
| 112 | CTL.HEAD(+)   | I   | Input signal from CTL HEAD(-)  | ---     | ---    | ---    | ---   |
| 113 | CTL.AMP.REF   | I   | Input for internal CTL amplifier reference voltage   | ---     | ---    | ---    | ---   |
| 114 | PB.CTL.OUT    | O   | Output from internal CLT amplifier   | ---     | ---    | ---    | ---   |
| 115 | +5V(A)        | I   | VDD for Analogue signal process.   | ---     | ---    | ---    | ---   |
| 116 | +5V(AD)       | I   | VDD for A/D converter.   | ---     | ---    | ---    | ---   |